REMARKS

The specification has been amended to completely describe the figures in the Brief Description of the Drawings section of the specification.

Examination on the merits is requested.

Respectfully submitted,

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VERSION WITH MARKINGS TO SHOW CHANGES MADE

Change paragraphs 8-11 on page 8 as follows:

[FIG. 8 is] <u>FIGS. 8A-8C are</u> explanatory diagrams showing the FFT operation of the DSP in FIG. 1;

[FIG. 9 is] <u>FIGS. 9A and 9B are</u> diagrams showing an embodiment of interpolating operation by a curve;

[FIG. 10 is] <u>FIGS. 10A and 10B are</u> explanatory diagrams showing the interpolating operation of the DSP in FIG. 1;

[FIG. 11 is] <u>FIGS. 11A and 11B are</u> diagrams for explaining interpolation functions;

On page 9, paragraph 1 change as follows:

[FIG. 12 is] <u>FIGS. 12A and 12B are</u> diagrams showing interpolating operation by an interpolation apparatus;

On page 9, paragraph 5 change as follows:

[FIG. 16 is] <u>FIGS. 16A and 16B are</u> diagrams showing another embodiment of interpolating operation by a curve;

On page 9, paragraph 8 change as follows:

[FIG. 19 is] <u>FIGS. 19A and 19B are</u> diagrams showing an embodiment of 20 interpolating operation by a straight line;

On page 9, paragraph 10 change as follows:

[FIG. 21 is] <u>FIGS. 21A and 21B are</u> diagrams showing another embodiment of 25 interpolating operation by a straight line;

On page 10, paragraph 2 change as follows:

[FIG. 23 is] FIGS. 23A-23C are diagrams for explaining the interpolating operation of a conventional variable low pass filter style;

On page 10, paragraph 3 change as follows:

[FIG 24 is] FIGS. 24A and 24B are diagrams for explaining the interpolating operation of a conventional variable low pass filter style;

On page 10, paragraph 5 change as follows:

[FIG. 26 is] FIGS. 26A and 26B are fundamental block diagrams showing the preliminary treatment and interpolation processing of a digital image signal;

On page 10, paragraph 6 change as follows:

[FIG. 27 is] FIGS. 27A-27C are conceptual diagrams of the transforming of a series of pixels in FIG. 26;

On page 10, paragraph 8 change as follows:

[FIG 29 is] FIGS. 29A-29G are timing charts showing the operation of the circuit in FIG. 28; and

On page 10, paragraph 9 change as follows:

[FIG. 30 is] FIGS. 30A-30C are drawings showing a transversal filter executing the two-dimensional interpolation processing of an image signal.